

AMENDMENT TO THE DISCLOSURE

Please amend the on page 13, lines 1-15, as follows.

Fig. 2 is a block diagram of the analyzer of the present invention referred to by the reference number 12. The analyzer 12 includes a computing display unit 32 and a receiver 34. The display unit 32 includes a microprocessor 36, a memory 37 including an interpreter 37A, a display 38, a personal interface 39, and a data interface 40A. The memory 37 may include solid state memory chips for storing programmed instructions for directing the microprocessor 36 and a hard disk for storing data files. The microprocessor 36 operates in a conventional manner for reading instructions and data in the memory 37; writing data into the memory 37; receiving information from the personal interface 39 and data interface 40A; and issuing information to the display 38 and the data interface 40A. In a preferred embodiment, the display unit 32 is a personal computer.

AMENDMENT TO THE DISCLOSURE

Please amend the paragraph on page 18, lines 12-30, as follows.

The protocol unit 56-58 for the complete operation of the Nth level protocol can represent several complete operations at a protocol level of Nth minus one (N-1) or lower. For example, the protocol unit 56-58 for the second level protocol can represent several operations at a first level protocol; the protocol unit 56-58 for a third level can represent several operations at the second level protocol; the protocol unit 56-58 for a fourth level protocol can represent several operations at the third level protocol; and so on. Also the protocol unit 56-58 for a third level can represent several operations at a first level protocol; the protocol unit 56-58 for a fourth level protocol can represent several operations at a second level protocol or several operations at a first level protocol, and so on. The protocol unit 56-58 for each level protocol can be individually expanded (by selecting downward arrowhead 63, Figs. 5A-B & 6A-B) or collapsed (by selecting upward arrowhead 64, Fig. 5C) using the personal interface 39 and selector 48 with the display presentation-48 46.